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NOTICE OF ALLOWANCE AND FEE(S) DUE

23446

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12/03/2009

MCANDREWS HELD & MALLOY, LTD 500 WEST MADISON STREET SUITE 3400 CHICAGO, IL 60661 EXAMINER
FRANKLIN, RICHARD B
ART UNIT PAPER NUMBER

2181

DATE MAILED: 12/03/2009

	APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
Ī	10/692,957	10/24/2003	Anand Pande	14920US01	2011

TITLE OF INVENTION: SYSTEM FOR DESIGNING DATA STRUCTURES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/03/2010

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

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If the SMALL ENTITY is shown as NO:

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III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

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Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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maintenance fee notifications. Note: A certificate of mailing can only be used for domestic mailings of the CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission. 23446 7590 12/03/2009 Certificate of Mailing or Transmission MCANDREWS HELD & MALLOY, LTD I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below. **500 WEST MADISON STREET SUITE 3400** CHICAGO, IL 60661 (Depositor's name (Signature (Date APPLICATION NO. FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. FILING DATE 10/692,957 10/24/2003 Anand Pande 14920US01 2011 TITLE OF INVENTION: SYSTEM FOR DESIGNING DATA STRUCTURES APPLN. TYPE SMALL ENTITY ISSUE FEE DUE PUBLICATION FEE DUE PREV. PAID ISSUE FEE TOTAL FEE(S) DUE DATE DUE nonprovisional NO \$1510 \$300 \$0 \$1810 03/03/2010 **EXAMINER** ART UNIT CLASS-SUBCLASS FRANKLIN, RICHARD B 710-052000 1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). 2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. or agents OR, alternatively, (2) the name of a single firm (having as a member a ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required. registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type) PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY) 4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) 4a. The following fee(s) are submitted: lssue Fee A check is enclosed. Publication Fee (No small entity discount permitted) Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number ______ (enclose an extra copy of this fo Advance Order - # of Copies _ (enclose an extra copy of this form). 5. Change in Entity Status (from status indicated above) a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ■ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2). NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office. Authorized Signature Date Typed or printed name Registration No. This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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MCANDREWS HELD & MALLOY, LTD			FRANKLIN, RICHARD B		
500 WEST MADISON STREET			ART UNIT	PAPER NUMBER	
SUITE 3400 CHICAGO, IL 60661			2181 DATE MAILED: 12/03/200	0	

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 294 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 294 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)	_
	10/692,957	PANDE, ANAND	
Notice of Allowability	Examiner	Art Unit	-
		2404	
	RICHARD FRANKLIN	2181	_
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate commu IGHTS. This application is s	this application. If not included unication will be mailed in due course. THIS	;
1. \boxtimes This communication is responsive to <u>the RCE and amended</u>	ment filed 23 September 200	<u>99</u> .	
2. The allowed claim(s) is/are 27-44.			
3. ☐ Acknowledgment is made of a claim for foreign priority unalled All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have	e been received.		
2. Certified copies of the priority documents have	• • • • • • • • • • • • • • • • • • • •		
3. Copies of the certified copies of the priority do	cuments have been received	d in this national stage application from the	
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requirements	
4. A SUBSTITUTE OATH OR DECLARATION must be subminFORMAL PATENT APPLICATION (PTO-152) which give			
5. CORRECTED DRAWINGS (as "replacement sheets") must	st be submitted.		
(a) \square including changes required by the Notice of Draftspers	son's Patent Drawing Reviev	v (PTO-948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date			
(b) ☐ including changes required by the attached Examiner'Paper No./Mail Date	s Amendment / Comment or	in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t			
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5 ☐ Notice of In	formal Patent Application	
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)		ummary (PTO-413),	
3. ☐ Information Disclosure Statements (PTO/SB/08),	Paper No./	Mail Date ´´ Amendment/Comment	
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit		Statement of Reasons for Allowance	
of Biological Material	9. ☐ Other		
/NIKETA I. PATEL/		-	_
Primary Examiner, Art Unit 2181			

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Michael Cruz (Reg. No. 44,636) on 19 November 2009.

The application has been amended as follows:

Amend the Title of the application to read:

SYSTEM FOR DESIGNING DATA STRUCTURES

Amend claim 27 to read:

A first-in-first-out (FIFO) data system, comprising:

a FIFO memory that has a binary write pointer input, a binary read pointer input, a write data input, a write clock input, and a read data output, wherein the FIFO memory comprises a data structure of depth d from which data may be written into using the write data input and read out using the read data output, wherein d is not a value of 2ⁿ in which d and n are integers;

an output of a first set of flip flops coupled to an input of a first gray-to-binary converter, an input of a first gray-code generator, an input of a second set of flip flops,

and an input of write clock logic that generates a first empty signal, a first full signal, and a first almost full signal;

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an output of the first gray-to-binary converter coupled to the binary write pointer input;

an output of the first gray-code generator coupled to an input of the first set of flip flops;

an output of the second set of flip flops coupled to an input of read clock logic that generates a second empty signal, a second full signal, and a second almost full signal;

an output of a third set of flip flops coupled to an input of a second gray-to-binary converter, an input of a second gray-code generator, an input of a fourth set of flip flops, and the input of the read clock logic;

an output of the second gray-code generator coupled to an input of the third set of flip flops;

an output of the second gray-to-binary converter coupled to the binary read pointer input; and

an output of the fourth set of flip flops coupled to the input of the write clock logic.

Amend claim 31 to read:

The FIFO data system according to claim 27,

wherein the first set of flip flops, the fourth set of flip flops, the first gray-to-binary converter, the first gray-code generator and the write clock logic are part of a write clock

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domain, wherein the second set of flip flops, the third set of flip flops, the second gray-to-binary converter, the second gray-code generator and the read clock logic are part of a read clock domain, and wherein the FIFO memory is part of both the write clock domain and the read clock domain.

Amend claim 35 to read:

The FIFO data system according to claim 27, wherein the second set of flip flops transmit first gray codes.

Amend claim 36 to read:

The FIFO data system according to claim 35, wherein the fourth set of flip flops transmit second gray codes.

DETAILED ACTION

2. Claims 27 – 44 are pending.

Continued Examination Under 37 CFR 1.114

3. A request for continued examination under 37 CFR 1.114 was filed in this application after a decision by the Board of Patent Appeals and Interferences, but before the filing of a Notice of Appeal to the Court of Appeals for the Federal Circuit or the commencement of a civil action. Since this application is eligible for continued examination under 37 CFR 1.114 and the fee set forth in 37 CFR 1.17(e) has been

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timely paid, the appeal has been withdrawn pursuant to 37 CFR 1.114 and prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on 23 September 2009 has been entered.

Allowable Subject Matter

- 4. Claims 27 44 are allowed.
- 5. The following is an examiner's statement of reasons for allowance:

Claims 27 – 44 are allowed because the prior art of record fails to teach or suggest alone or in combination a first-in-first-out (FIFO) data system, comprising: a FIFO memory that has a binary write pointer input, a binary read pointer input, a write data input, a write clock input, and a read data output, wherein the FIFO memory comprises a data structure of depth d from which data may be written into using the write data input and read out using the read data output, wherein d is not a value of 2ⁿ in which d and n are integers; an output of a first set of flip flops coupled to an input of a first gray-to-binary converter, an input of a first gray-code generator, an input of a second set of flip flops, and an input of write clock logic that generates a first empty signal, a first full signal, and a first almost full signal; an output of the first gray-to-binary converter coupled to the binary write pointer input; an output of the first gray-code generator coupled to an input of the first set of flip flops; an output of the second set of flip flops coupled to an input of read clock logic that generates a second empty signal, a second full signal, and a second almost full signal; an output of a third set of flip flops

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coupled to an input of a second gray-to-binary converter, an input of a second gray-code generator, an input of a fourth set of flip flops, and the input of the read clock logic; an output of the second gray-code generator coupled to an input of the third set of flip flops; an output of the second gray-to-binary converter coupled to the binary read pointer input; and an output of the fourth set of flip flops coupled to the input of the write clock logic, as required by independent claim 27, in combination with other claimed limitations (emphasis added). Support for the above limitations can be found in the originally filed specification in Figure 1 and the accompanying detailed description of the figure. The prior art of record teaches using gray codes in addressing a FIFO memory which operates multiple different clock domains, but does not teach the circuit layout described in the claims to perform the addressing.

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Claims 28 – 44 are allowed because of their dependency upon allowed independent claim 27.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to RICHARD FRANKLIN whose telephone number is (571)272-0669. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Alford Kindred can be reached on (571) 272-4037. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Richard Franklin /RBF/ Patent Examiner Art Unit 2181

/NIKETA I. PATEL/ Primary Examiner, Art Unit 2181